

PARALLEL COMPUTER ARCHITECTURE



A Hardware / Software Approach



David E. Culler
Jaswinder Pal Singh
with Anoop Gupta

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James M. Ortega, Robert G. Voigt



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Voigt, 1985-01-01 This volume reviews in the context of partial differential equations algorithm development that has been specifically aimed at computers that exhibit some form of parallelism Emphasis is on the solution of PDEs because these are typically the problems that generate high computational demands The authors discuss architectural features of these computers inasmuch as they influence algorithm performance and provide insight into algorithm characteristics that allow effective use of hardware *Computer Organization, Design, and Architecture* Sajjan G. Shiva, 2007-11-30 Suitable for a one or two semester undergraduate or beginning graduate course in computer science and computer engineering Computer Organization Design and Architecture Fourth Edition presents the operating principles capabilities and limitations of digital computers to enable development of complex yet efficient systems With 40% upd **Parallel Computer Architecture: A**

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Parallel Computer Architecture David E. Culler, 2000 **Shared Memory Multiprocessing** Norihisa Suzuki, 1992

Shared memory multiprocessors are becoming the dominant architecture for small scale parallel computation This book is the first to provide a coherent review of current research in shared memory multiprocessing in the United States and Japan It focuses particularly on scalable architecture that will be able to support hundreds of microprocessors as well as on efficient and economical ways of connecting these fast microprocessors The 20 contributions are divided into sections covering the experience to date with multiprocessors cache coherency software systems and examples of scalable shared memory multiprocessors *High Performance Computing - HiPC'99* Prith Banerjee, Viktor K. Prasanna, Bhabani P. Sinha, 2004-06-01 These are the proceedings of the Sixth International Conference on High Performance Computing HiPC 99 held December 17 20 in Calcutta India The meeting serves as a forum for presenting current work by researchers from around the world as well as highlighting activities in Asia in the high performance computing area The meeting emphasizes both the design and the analysis of high performance computing systems and their scientific engineering and commercial applications Topics covered in the meeting series include Parallel Algorithms Scientific Computation Parallel Architectures Visualization Parallel Languages Compilers Network and Cluster Based Computing Distributed Systems Signal Image Processing Systems Programming Environments Supercomputing Applications Memory Systems Internet and WWW based Computing Multimedia and High Speed Networks Scalable Servers We would like to thank Alfred Hofmann and Ruth Abraham of

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Proceedings of the Twenty-Second Annual ACM Symposium on Principles of Distributed Computing ,2003 This paper presents an efficient asynchronous protocol to compute RSA inverses with respect to a public RSA modulus N whose factorization is secret and shared among a group of parties Given two numbers x and e the protocol computes y such that $ye \equiv x \pmod{N}$ A synchronous protocol for this task has been presented by Catalano Gennaro and Halevi Eurocrypt 2000 but the standard approach for turning this into an asynchronous protocol would require a Byzantine agreement sub protocol Our protocol adopts their approach but exploits a feature of the problem in order to avoid the use of a Byzantine agreement primitive Hence it leads to efficient asynchronous protocols for threshold signatures and for Byzantine agreement based on the strong RSA assumption without the use of random oracles

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