

DEVICE ELECTRONICS
FOR
INTEGRATED
CIRCUITS

Second Edition

Richard S. Muller
Theodore I. Kamins

Device Electronics For Integrated Circuits 2nd Edition

Niantsu Wang

A decorative graphic element consisting of a semi-circular red-to-white gradient shape on the right side of a light blue horizontal bar.

Device Electronics For Integrated Circuits 2nd Edition:

Device Electronics for Integrated Circuits (2nd.ed.), RICHARD S. MULLER, Device Electronics for Integrated Circuits Richard S. Muller, Theodore I. Kamins, 1977 Focusing specifically on silicon devices the Third Edition of Device Electronics for Integrated Circuits takes students in integrated circuits courses from fundamental physics to detailed device operation Because the book focuses primarily on silicon devices each topic can include more depth and extensive worked examples and practice problems ensure that students understand the details *Device Electronics for Integrated Circuits* Richard S. Muller, Theodore I. Kamins, 2002-10-28 Focusing specifically on silicon devices the Third Edition of Device Electronics for Integrated Circuits takes students in integrated circuits courses from fundamental physics to detailed device operation Because the book focuses primarily on silicon devices each topic can include more depth and extensive worked examples and practice problems ensure that students understand the details

Modern Semiconductor Devices for Integrated Circuits Chenming Hu, 2010 For courses in semiconductor devices Prepare your students for the semiconductor device technologies of today and tomorrow Modern Semiconductor Devices for Integrated Circuits First Edition introduces students to the world of modern semiconductor devices with an emphasis on integrated circuit applications Written by an experienced teacher researcher and expert in industry practices this succinct and forward looking text is appropriate for both undergraduate and graduate students and serves as a suitable reference text for practicing engineers Device Electronics for Integrated Circuits Richard S. Muller, Theodore I. Kamins, 1986 This Second Edition provides all the required information for a course in modern device electronics taken by undergraduate electrical engineers Offers major new coverage of silicon technology adds several topics in basic semiconductor physics not treated previously and introduces Hall effect sensors The chapters on MOSFET have been entirely updated focusing on mobility variations and threshold voltage dependence Additional topics include VLSI devices short channel effects and computer modeling

Principles of Semiconductor Devices Sima Dimitrijevic, 2012 The dimensions of modern semiconductor devices are reduced to the point where classical semiconductor theory including the concepts of continuous particle concentration and continuous current becomes questionable Further questions relate to two dimensional transport in the most important field effect devices and one dimensional transport in nanowires and carbon nanotubes Designed for upper level undergraduate and graduate courses Principles of Semiconductor Devices Second Edition presents the semiconductor physics and device principles in a way that upgrades classical semiconductor theory and enables proper interpretations of numerous quantum effects in modern devices The semiconductor theory is directly linked to practical applications including the links to the SPICE models and parameters that are commonly used during circuit design The text is divided into three parts Part I explains semiconductor physics Part II presents the principles of operation and modeling of the fundamental junctions and transistors and Part III provides supplementary topics including a dedicated chapter on the physics of nanoscale devices description of the SPICE models and

equivalent circuits that are needed for circuit design introductions to the most important specific devices photonic devices JFETs and MESFETs negative resistance diodes and power devices and an overview of integrated circuit technologies The chapters and the sections in each chapter are organized so as to enable instructors to select more rigorous and design related topics as they see fit

BiCMOS Technology and Applications Antonio R. Alvarez, 2012-12-06 BiCMOS Technology and Applications Second Edition provides a synthesis of available knowledge about the combination of bipolar and MOS transistors in a common integrated circuit BiCMOS In this new edition all chapters have been updated and completely new chapters on emerging topics have been added In addition BiCMOS Technology and Applications Second Edition provides the reader with a knowledge of either CMOS or Bipolar technology design a reference with which they can make educated decisions regarding the viability of BiCMOS in their own application BiCMOS Technology and Applications Second Edition is vital reading for practicing integrated circuit engineers as well as technical managers trying to evaluate business issues related to BiCMOS As a textbook this book is also appropriate at the graduate level for a special topics course in BiCMOS A general knowledge in device physics processing and circuit design is assumed Given the division of the book it lends itself well to a two part course one on technology and one on design This will provide advanced students with a good understanding of tradeoffs between bipolar and MOS devices and circuits

Design of Analog CMOS Integrated Circuits Behzad Razavi, 2016-01-22 The CMOS technology are has quickly grown calling for a new text and here it is covering the analysis and design of CMOS integrated circuits that practicing engineers need to master to succeed Filled with many examples and chapter ending problems the book not only describes the thought process behind each circuit topology but also considers the rationale behind each modification The analysis and design techniques focus on CMOS circuits but also apply to other IC technologies Design of Analog CMOS Integrated Circuits deals with the analysis and design of analog CMOS integrated circuits emphasizing recent technological developments and design paradigms that students and practicing engineers need to master to succeed in today s industry Based on the author s teaching and research experience in the past ten years the text follows three general principles 1 Motivate the reader by describing the significance and application of each idea with real world problems 2 Force the reader to look at concepts from an intuitive point of view preparing him her for more complex problems 3 Complement the intuition by rigorous analysis confirming the results obtained by the intuitive yet rough approach

Integrated Audio Amplifiers in BCD Technology Marco Berkhout, 2012-12-06 Integrated Audio Amplifiers in BCD Technology is the first book to describe the design at Audio Amplifiers using a Bipolar CMOS DMOS BCD process It shows how the combination of the 3 processes made available by advances in process technology gives rise to the design of more robust and powerful audio amplifiers which can be more easily implemented in digital and mixed signal circuits Integrated Audio Amplifiers in BCD Technology starts with an introduction to audio amplifiers which includes a comparison of amplifier classes general design considerations and a list of specifications for integrated audio power

amplifiers This is followed by an extensive discussion of the properties of DMOS transistors which are the key components in BCD technologies Then the theory and the design of chargepump circuits is considered In most BCD technologies only n type DMOS transistors are available Therefore a boosted supply voltage is required to achieve rail to rail output capability which can be generated with a chargepump The new solutions that are found can also be used for many applications where DC DC conversion with low output ripple is needed Finally the design of audio power amplifier in BCD technology is discussed The design concentrates on a new quiescent control circuit with very high ratio between quiescent current and maximum output current and on the output stage topologies The problem of controlling the DMOS output transistors over a wide range of currents either saturated or non saturated requires a completely new design of the driving circuits that utilize of the special properties of the DMOS transistor Integrated Audio Amplifiers in BCD Technology is essential reading for practising analog design engineers and researchers in the field It is also suitable as a text for an advanced course on the subject With a foreword by Ed van Tuijl

Device Circuit Co-Design Issues in FETs Shubham Tayal,Billel Smaani,Shiromani Balmukund Rahi,Samir Labiod,Zeinab Ramezani,2023-08-22 This book provides an overview of emerging semiconductor devices and their applications in electronic circuits which form the foundation of electronic devices Device Circuit Co Design Issues in FETs provides readers with a better understanding of the ever growing field of low power electronic devices and their applications in the wireless biosensing and circuit domains The book brings researchers and engineers from various disciplines of the VLSI domain together to tackle the emerging challenges in the field of engineering and applications of advanced low power devices in an effort to improve the performance of these technologies The chapters examine the challenges and scope of FinFET device circuits 3D FETs and advanced FET for circuit applications The book also discusses low power memory design neuromorphic computing and issues related to thermal reliability The authors provide a good understanding of device physics and circuits and discuss transistors based on the new channel dielectric materials and device architectures to achieve low power dissipation and ultra high switching speeds to fulfill the requirements of the semiconductor industry This book is intended for students researchers and professionals in the field of semiconductor devices and nanodevices as well as those working on device circuit co design issues

Semiconductor Device Physics and Simulation J.S. Yuan,Juin Jei Liou,1998-05-31 The advent of the microelectronics technology has made ever increasing numbers of small devices on a same chip The rapid emergence of ultra large scaled integrated ULSI technology has moved device dimension into the sub quarter micron regime and put more than 10 million transistors on a single chip While traditional closed form analytical models furnish useful intuition into how semiconductor devices behave they no longer provide consistently accurate results for all modes of operation of these very small devices The reason is that in such devices various physical mechanisms affect the device performance in a complex manner and the conventional assumptions i e one dimensional treatment low level injection quasi static approximation etc employed in developing analytical models become

questionable Thus the use of numerical device simulation becomes important in device modeling Researchers and engineers will rely even more on device simulation for device design and analysis in the future This book provides comprehensive coverage of device simulation and analysis for various modern semiconductor devices It will serve as a reference for researchers engineers and students who require in depth up to date information and understanding of semiconductor device physics and characteristics The materials of the book are limited to conventional and mainstream semiconductor devices photonic devices such as light emitting and laser diodes are not included nor does the book cover device modeling device fabrication and circuit applications

Semiconductor Devices and Integrated Electronics A. G. Milnes, 2012-12-06 For some time there has been a need for a semiconductor device book that carries diode and transistor theory beyond an introductory level and yet has space to touch on a wider range of semiconductor device principles and applications Such topics are covered in specialized monographs numbering many hundreds but the voluminous nature of this literature limits access for students This book is the outcome of attempts to develop a broad course on devices and integrated electronics for university students at about senior year level The educational prerequisites are an introductory course in semiconductor junction and transistor concepts and a course on analog and digital circuits that has introduced the concepts of rectification amplification oscillators modulation and logic and Switching circuits The book should also be of value to professional engineers and physicists because of both the information included and the detailed guide to the literature given by the references The aim has been to bring some measure of order into the subject area examined and to provide a basic structure from which teachers may develop themes that are of most interest to students and themselves Semiconductor devices and integrated circuits are reviewed and fundamental factors that control power levels frequency speed size and cost are discussed The text also briefly mentions how devices are used and presents circuits and comments on representative applications Thus the book seeks a balance between the extremes of device physics and circuit design

VLSI Electronics: Advanced MOS device physics Norman G. Einspruch, 1989 [Compact Models for Integrated Circuit Design](#) Samar K. Saha, 2018-09-03 [Compact Models for Integrated Circuit Design](#) Conventional Transistors and Beyond provides a modern treatise on compact models for circuit computer aided design CAD Written by an author with more than 25 years of industry experience in semiconductor processes devices and circuit CAD and more than 10 years of academic experience in teaching compact modeling courses this first of its kind book on compact SPICE models for very large scale integrated VLSI chip design offers a balanced presentation of compact modeling crucial for addressing current modeling challenges and understanding new models for emerging devices Starting from basic semiconductor physics and covering state of the art device regimes from conventional micron to nanometer this text Presents industry standard models for bipolar junction transistors BJTs metal oxide semiconductor MOS field effect transistors FETs FinFETs and tunnel field effect transistors TFETs along with statistical MOS models Discusses the major issue of process variability which severely impacts device and

circuit performance in advanced technologies and requires statistical compact models Promotes further research of the evolution and development of compact models for VLSI circuit design and analysis Supplies fundamental and practical knowledge necessary for efficient integrated circuit IC design using nanoscale devices Includes exercise problems at the end of each chapter and extensive references at the end of the book Compact Models for Integrated Circuit Design Conventional Transistors and Beyond is intended for senior undergraduate and graduate courses in electrical and electronics engineering as well as for researchers and practitioners working in the area of electron devices However even those unfamiliar with semiconductor physics gain a solid grasp of compact modeling concepts from this book

Tungsten and Other Refractory Metals for VLSI Applications IV Robert S. Blewer, Carol M. McConica, 1989 Proceedings, 1995 Digital Integrated Circuits John E. Ayers, 2018-09-03 Exponential improvement in functionality and performance of digital integrated circuits has revolutionized the way we live and work The continued scaling down of MOS transistors has broadened the scope of use for circuit technology to the point that texts on the topic are generally lacking after a few years The second edition of Digital Integrated Circuits Analysis and Design focuses on timeless principles with a modern interdisciplinary view that will serve integrated circuits engineers from all disciplines for years to come Providing a revised instructional reference for engineers involved with Very Large Scale Integrated Circuit design and fabrication this book delves into the dramatic advances in the field including new applications and changes in the physics of operation made possible by relentless miniaturization This book was conceived in the versatile spirit of the field to bridge a void that had existed between books on transistor electronics and those covering VLSI design and fabrication as a separate topic Like the first edition this volume is a crucial link for integrated circuit engineers and those studying the field supplying the cross disciplinary connections they require for guidance in more advanced work For pedagogical reasons the author uses SPICE level 1 computer simulation models but introduces BSIM models that are indispensable for VLSI design This enables users to develop a strong and intuitive sense of device and circuit design by drawing direct connections between the hand analysis and the SPICE models With four new chapters more than 200 new illustrations numerous worked examples case studies and support provided on a dynamic website this text significantly expands concepts presented in the first edition

Digital MOS Integrated Circuits Niantzu Wang, 1989 Very Good No Highlights or Markup all pages are intact Amorphous Insulating Thin Films, 1993 **Digital Timing Macromodeling for VLSI Design Verification** Jeong-Taek Kong, David V. Overhauser, 1995-05-31 Digital Timing Macromodeling for VLSI Design Verification first of all provides an extensive history of the development of simulation techniques It presents detailed discussion of the various techniques implemented in circuit timing fast timing switch level timing switch level and gate level simulation It also discusses mixed mode simulation and interconnection analysis methods The review in Chapter 2 gives an understanding of the advantages and disadvantages of the many techniques applied in modern digital macromodels The book also presents a wide variety of techniques for performing nonlinear macromodeling of

digital MOS subcircuits which address a large number of shortcomings in existing digital MOS macromodels. Specifically, the techniques address the device model detail: transistor coupling capacitance, effective channel length modulation, series transistor reduction, effective transconductance, input terminal dependence, gate parasitic capacitance, the body effect, the impact of parasitic RC interconnects, and the effect of transmission gates. The techniques address major sources of errors in existing macromodeling techniques which must be addressed if macromodeling is to be accepted in commercial CAD tools by chip designers. The techniques presented in Chapters 4-6 can be implemented in other macromodels and are demonstrated using the macromodel presented in Chapter 3. The new techniques are validated over an extremely wide range of operating conditions, much wider than has been presented for previous macromodels, thus demonstrating the wide range of applicability of these techniques.

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