

CBGS SCHEME



USN

18EC35

Third Semester B.E. Degree Examination, Aug./Sept.2020 Computer Organization and Architecture

Time: 3 hrs.

Max. Marks: 100

Note: Answer any FIVE full questions, choosing ONE full question from each module.

Module-1

- 1 a. Explain the operation of computer with neat block diagram. (10 Marks)
b. Explain computer basic performance equation. (04 Marks)
c. Explain following with an example : i) Three – address instruction
ii) Two – address instruction iii) One – address instruction. (06 Marks)

OR

- 2 a. Explain Single –BUS structure in computer. (06 Marks)
b. Explain system software functions in computer. (06 Marks)
c. What is Operating system? Explain user program and OS routine sharing the processor. (08 Marks)

Module-2

- 3 a. Explain Big-Endian and Little-Endian with neat diagram. (08 Marks)
b. Explain memory operations with examples. (04 Marks)
c. Explain condition codes with examples. (08 Marks)

OR

- 4 a. Discuss following addressing modes with example :
i) Immediate ii) Register iii) Direct iv) Indirect v) Index. (10 Marks)
b. What are assembler directive? Explain any five assembler directives. (10 Marks)

Module-3

- 5 a. With a neat diagram, explain how to interface printer to the processor. (10 Marks)
b. Define Interrupt, Point out and explain the various ways of enabling and disabling interrupts. (10 Marks)

OR

- 6 a. Explain the following method of handling interrupts from multiple devices.
i) Daisy chain method ii) Priority structure. (10 Marks)
b. Explain operation of DMA with neat diagram. (10 Marks)

Module-4

- 7 a. Explain internal organization of 16×8 memory chip. (10 Marks)
b. Discuss a single-transistor dynamic memory cell. (06 Marks)
c. Write a note on Virtual Memory. (04 Marks)

OR

- 8 a. Draw and explain the internal organization of $2M \times 8$ asynchronous DRAM Chip. (08 Marks)
b. Describe the principles of magnetic disk. (06 Marks)
c. What is mapping? Explain set associative cache mapping techniques. (06 Marks)

Module-5

- 9 a. Discuss with neat diagram, the single bus organization of data path inside a processor. (10 Marks)
b. What are the action required to execute a complete instruction ADD(R3), R1. (10 Marks)

OR

- 10 a. Draw and explain multiple bus organization of CPU. (10 Marks)
b. Draw and explain organization of the control unit to allow conditional branching in the microprogram. (10 Marks)

Computer Organization And Architecture Objective Question Answer

J Dewey



Computer Organization And Architecture Objective Question Answer:

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Security+ All-in-One Exam Guide, Fifth Edition (Exam SY0-501) Wm. Arthur Conklin, Greg White, Dwayne Williams, Chuck Cothren, Roger L. Davis, 2018-01-05 This fully updated study guide covers every topic on the current version of the CompTIA Security exam Take the latest version of the CompTIA Security exam with complete confidence using the detailed information contained in this highly effective self study system Written by a team of leading information security experts this authoritative guide addresses the skills required for securing a network and managing risk and enables you to become CompTIA Security certified CompTIA Security All in One Exam Guide Fifth Edition Exam SY0 501 covers all exam domains and features 200 accurate practice questions To aid in study the book features learning objectives at the beginning of each chapter exam tips practice exam questions and in depth explanations All questions mirror those on the live test in style format and difficulty Beyond fully preparing you for the challenging SY0 501 exam the book also serves as a valuable on the job reference for IT professionals Provides 100% coverage of every objective on exam SY0 501 Electronic content includes 200 practice questions and a secured book PDF Written by a team of experienced IT security academics

Distributed Computer Control Systems Thomas J. Harrison, 1980 Distributed Computer Control Systems Proceedings of the IFAC Workshop Tampa Florida U S A 2 4 October 1979 focuses on the design processes methodologies and applications of distributed computing systems

Computer Science J. Glenn Brookshear, 1997 An introduction to computer science Using real life analogies and examples this text introduces coverage of the World Wide Web and the Java programming language and includes a larger emphasis on the object oriented paradigm and networking

Fourth Workshop on Computer Architecture for Non-numeric Processing, 1978 *International Workshop on Computer Architecture, Grenoble, June 26-28th, 1973*, 1973

Architecture Exam Review: Nonstructural topics David Kent Ballast, 1998

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