

- (f) What is floating point number? What is the difference between Single precision and double precision floating point numbers? What is a representation for Zero in IEEE 754 single precision standard. Represent $(-23456.05)_{10}$ and $(0.00025)_{10}$ in IEEE 754 single precision format. (4 Marks)

Question 2 (Covers Block 2)

- (a) A RAM has 1M rows each having 16 cells;
- (i) How many data input and data output lines does this RAM needs? Explain your answer.
 - (ii) What is the capacity of RAM in bytes.
 - (iii) How many address lines are needed for this RAM? Give reason in support of your answer. (2 Marks)
- (b) A computer has 1 MB RAM and has a word size of 16 bits. It has cache memory having 8 blocks having a block size of 32 bits. Show how the main memory address 0001 1001 1110 1101 0001 will be mapped to cache address, if
- (i) Direct cache mapping is used
 - (ii) Associative cache mapping is used
 - (iii) Two way set associative cache mapping is used. (4 Marks)
- (c) What is the need of DMA in a Computer? How is DMA different than that of Interrupt driven I/O technique? Assume that a new computer supports few bytes of input from multiple user's, which of the three I/O techniques is most suitable for this computer? Justify your answer. (4 Marks)
- (d) Assume that a disk has 1000 tracks with each track having 64 sectors and each sector is of size 512 K. A file having the name *bcawca.txt* is of size 16 M. Assume that disk has four free continuous clusters of 8 sectors each. How can this file be given the space on the disk? Also show the content of FAT after the space allocation to this file. You may make suitable assumptions. You may assume the cluster size as 2 sectors, if needed. (4 Marks)
- (e) Explain the following giving their uses and advantages/disadvantages. (Word limit for answer of each part is 50 words ONLY) (6 Marks)
- (i) Memory Interleaving
 - (ii) RAID
 - (iii) IDE interface
 - (iv) CAV and CLV in the context of disks
 - (v) Passive and Active matrix display
 - (vi) Advantages and disadvantages of Ink-jet printers

Question 3 (Covers Block 3)

- (a) A computer has a single core processor having 8 General purpose registers and 8 additional special purpose registers. The machine has 64 KB RAM. The size of each register and memory word is 16 bits each. An instruction of the machine is of fixed length and is equal to two memory words. Each instruction of the machine has two operands – one memory operand and second register operand. Memory

Computer Organisation Solved Paper

Rachel Sandford



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Table of Contents Computer Organisation Solved Paper

1. Understanding the eBook Computer Organisation Solved Paper
 - The Rise of Digital Reading Computer Organisation Solved Paper
 - Advantages of eBooks Over Traditional Books
2. Identifying Computer Organisation Solved Paper
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Computer Organisation Solved Paper
 - User-Friendly Interface
4. Exploring eBook Recommendations from Computer Organisation Solved Paper
 - Personalized Recommendations
 - Computer Organisation Solved Paper User Reviews and Ratings
 - Computer Organisation Solved Paper and Bestseller Lists
5. Accessing Computer Organisation Solved Paper Free and Paid eBooks
 - Computer Organisation Solved Paper Public Domain eBooks
 - Computer Organisation Solved Paper eBook Subscription Services
 - Computer Organisation Solved Paper Budget-Friendly Options

6. Navigating Computer Organisation Solved Paper eBook Formats
 - ePub, PDF, MOBI, and More
 - Computer Organisation Solved Paper Compatibility with Devices
 - Computer Organisation Solved Paper Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Computer Organisation Solved Paper
 - Highlighting and Note-Taking Computer Organisation Solved Paper
 - Interactive Elements Computer Organisation Solved Paper
8. Staying Engaged with Computer Organisation Solved Paper
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Computer Organisation Solved Paper
9. Balancing eBooks and Physical Books Computer Organisation Solved Paper
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Computer Organisation Solved Paper
10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Computer Organisation Solved Paper
 - Setting Reading Goals Computer Organisation Solved Paper
 - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Computer Organisation Solved Paper
 - Fact-Checking eBook Content of Computer Organisation Solved Paper
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
 - Integration of Multimedia Elements

- Interactive and Gamified eBooks

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