

10. True/False and multiple choice questions.

Part a) (each 1 point)

No.	Question	True	False
1	A gate or set of gates is universal if it can be used to construct any Boolean function. Is the set of gates {AND, NOT} universal?		
2	The Boolean function $F(A,B,C) = A+BC$ is in SOP form.		
3	Having a L2 cache helps by reducing the miss rate in L1 cache.		
4	Page Table Resides in Secondary Memory.		
5	Page Faults are handled by the Operating System.		
6	Pipelining a processor helps by reducing the throughput.		

Part b) (each 2 points)

- The total amount of work done during the execution, in a given time is referred as
 - a. Response Time
 - b. Execution Time
 - c. Throughput
 - d. Delay Time

- In MIPS architecture, which instruction causes a subtraction operation in ALU?
 - a. Lw \$t0,4(\$s0)
 - b. Beq \$t0,\$t1,loop
 - c. Sw \$t0,4(\$s0)
 - d. Add \$t0,\$t1,\$t2

Chapter 1 Test True False Multiple Choice

Clemens Wendtner



Chapter 1 Test True False Multiple Choice:

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