

operating bias region for each condition?  $V_{DD} = 5\text{ V}$ ,  $R_D = 3\text{ k}\Omega$ . What is the



Figure P3.27

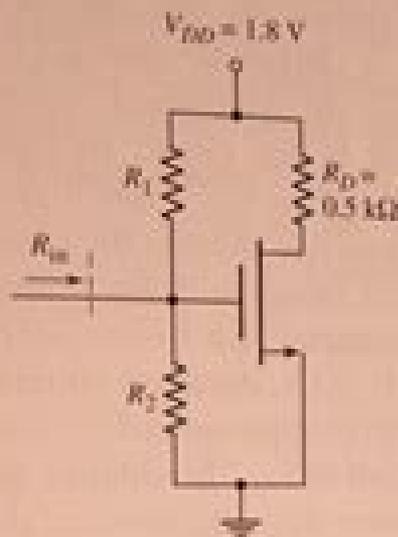


Figure P3.28



Figure P3.29

Figure P3.26

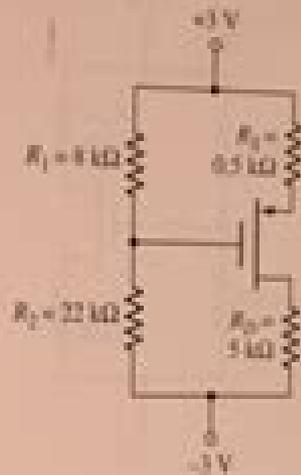


Figure P3.30

3.28 The transistor in Figure P3.28 has parameters  $V_{TN} = 0.4\text{ V}$ ,  $k'_n = 120\ \mu\text{A/V}^2$ , and  $W/L = 80$ . Design the circuit such that  $I_Q = 0.8\text{ mA}$  and  $R_s = 200\text{ k}\Omega$ .

3.29 The transistor in the circuit in Figure P3.29 has parameters  $V_{TF} = -0.8\text{ V}$  and  $K_p = 0.20\text{ mA/V}^2$ . Sketch the load line and plot the  $Q$ -point for (a)  $V_{DD} = 3.5\text{ V}$ ,  $R_D = 1.2\text{ k}\Omega$  and (b)  $V_{DD} = 5\text{ V}$ ,  $R_D = 4\text{ k}\Omega$ . What is the operating bias region for each condition?

3.30 Consider the circuit in Figure P3.30. The transistor parameters are  $V_{TF} = -0.8\text{ V}$  and  $K_p = 0.5\text{ mA/V}^2$ . Determine  $I_D$ ,  $V_{SG}$ , and  $V_{SD}$ .

3.31 For the circuit in Figure P3.31, the transistor parameters are  $V_{TF} = -0.8\text{ V}$  and  $K_p = 200\ \mu\text{A/V}^2$ . Determine  $V_S$  and  $V_{SD}$ .

3.32 Design a MOSFET circuit in the configuration shown in Figure P3.26. The transistor parameters are  $V_{TN} = 0.4\text{ V}$  and  $k'_n = 120\ \mu\text{A/V}^2$ , and  $\lambda = 0$ . The circuit

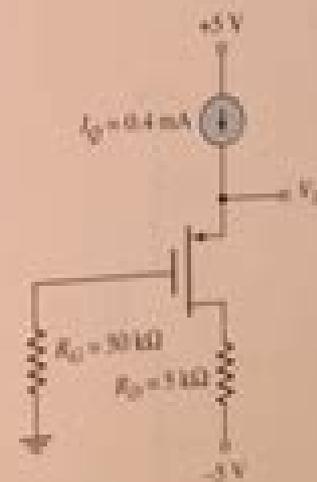


Figure P3.31

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**IM Harris**



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