



Time Allotted : 3 Hours

Full Marks : 70

The Figures in the margin indicate full marks
Candidates are required to give their answers in their own words as far as practicable

Group-A (Very Short Answer Type Question)

1. Answer any ten of the following :

[10 x 10 = 100]

- Q1 In which one of the following addressing modes is the actual operand provides as a part of the instruction?
1. Direct
2. Immediate
3. Indirect
4. Relative
- Q2 What is micro-operation?
- Q3 Convert the infix notation $A \times B + C \times D - E \times F$ into Postfix Notation or Reverse Polish Notation?
- Q4 What is the full form of SISO?
- Q5 Input or output devices that are connected to computer are called _____
- Q6 What is Tag Field?
- Q7 Which one of the following most correctly describes the functionality of the control unit in a CPU?
a. To perform arithmetic operations based on decoded program instruction.
b. To store program instruction.
c. To perform logic operations based on decoded program instruction.
d. To generate control signals based on decoded program instruction.
- Q8 What is micro programmed control?
- Q9 The electrical circuits/lines that you see on the motherboard that is used to transfer data is known as _____
- Q10 What is the Hazard in pipeline?
- Q11 The sign magnitude representation of -1 is _____
- Q12 The subtraction result of -7 and 1 in 2's complement form is _____

Group-B (Short Answer Type Question)

Answer any three of the following :

[3 x 10 = 30]

- 2. Multiply (20) AND (-19) Using BOOTH'S Algorithm with register size 5.
- 3. Derive the control gates associated with program counter PC in the basic computer?
- 4. Find out the 10^3 complement of 54670.
- 5. Draw the typical RAM & ROM chip with proper explanation.
- 6. Convert binary number 1100010 into hexa-decimal number.

10
10
10
15
15

Group-C (Long Answer Type Question)

Answer any three of the following :

[3 x 15 = 45]

- 7. Explain Booth's algorithm with the flow chart.
Multiply (-5) and (4) using booth's algorithm with register size 4.
- 8. Explain stack register.
Write down the micro-operations for push & pop.
Explain reverse polish notation with a example.
- 9. Explain the rules for converting a decimal number into floating point.
Explain IEEE 754 format to represent floating point data.
Convert 39887.5625 to IEEE 32-bit floating point format.
- 10. What is rise instruction format?
Explain different type of addressing modes.
What is subroutine call.

15+15
15+5+5
15+5+5
15+8+4

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Wenbin Ji



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MCS-012: Computer Organisation and Assembly Language Programming Dr. DK Sukhani, This book is useful for IGNOU BCA MCA students A perusal of past questions papers gives an idea of the type of questions asked the paper pattern and so on it is for this benefit we provide these IGNOU MCS 012 Computer Organisation and Assembly Language Programming Notes Students are advised to refer these solutions in conjunction with their reference books It will help you to improve your exam preparations This book covers Combination Circuits Logic Gates Sequential circuits Registers I O Interface Instruction and Addressing CPU design Memory systems virtual and cache memory Input and Output Organization Asynchronus data transfer Direct Memory access I O processors serial communication Basics of Computer Organization System buses and instruction cycles memory susbsytem organizing and interfacing and much more Published by MeetCoogole

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