

QUANTILE TUTORIAL

DEPTING LOGIC CIRCUIT MODELS AND SIMULATIONS WITH SPICE

Multisim is a schematic capture and simulation program for creating, simulating and viewing digital circuits, and is the application program of the National Instruments "Circuit Design Suite", which also includes printed-circuit board design tools and an interface to the EDA manufacturing platform.

It is suggested that you begin by taking a few minutes to read the short Multisim tutorial, which is presented as Chapter 2 of "Getting Started with the Circuit Design Suite", which can be accessed via

[Resources > Getting Started > National Instruments > Circuit Design Suite 12.0 > User Manuals > Getting Started](#)

This page aims to describe and explain all a digital logic circuit can:

1. Open Multisim and create a "Design".
2. Place a schematic diagram of the circuit components and interconnections.
3. Define digital test patterns to be applied to the circuit inputs to stimulate the circuit and connect digital outputs to the inputs to produce these patterns.
4. Connect the circuit outputs to one or more indicators to display the responses of the circuit to the test patterns.
5. Run the simulation and examine the results, pausing and pausing Multisim windows and test reports and other documents as needed.
6. Save the design.

Step 1. Open Multisim and create a Design

Multisim is opened from the Start Menu:

[Start Menu > All Programs > National Instruments > Circuit Design Suite 12.0 > Multisim 12.0 > Multisim](#)

This creates a blank design called "Design1", as illustrated in Figure 1. Note that the width the circuit design pane is much less than **MS-Word** (see to see that standard Windows task dialog shown in Figure 2). Navigate to the directory in which you want to save your design, enter the desired file name, and click the **Save** button. The default file extension for Multisim 12.0 design files is ".ms10".

Note: Create a separate directory for your design or your network drive (N:) to be used exclusively for storing your Multisim design and report files. This will ensure that (1) your files will not conflict with any file conflicts and (2) your Multisim files will not be mixed up with your other files. It is also a good idea to periodically make backup copies of your files on removable storage such as USB.

A previously created design can be opened via **MS-Word**. In the dialog window, navigate to the directory in which the design is stored, select the file, and click the **Open** button.

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R Sandford



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Quantum-Dot Cellular Automata Based Digital Logic Circuits: A Design Perspective Trailokya Nath Sasamal, Ashutosh Kumar Singh, Anand Mohan, 2019-12-16 This book covers several futuristic computing technologies like quantum computing quantum dot cellular automata DNA computing and optical computing In turn it explains them using examples and tutorials on a CAD tool that can help beginners get a head start in QCA layout design It discusses research on the design of circuits in quantum dot cellular automata QCA with the objectives of obtaining low complexity robust designs for various arithmetic operations The book also investigates the systematic reduction of majority logic in the realization of multi bit adders dividers ALUs and memory

Rapid Prototyping of Digital Systems James O. Hamblen, Tyson S. Hall, Michael D. Furman, 2006-01-16 Rapid Prototyping of Digital Systems Quartus II Edition provides an exciting and challenging laboratory component for undergraduate digital logic and computer design courses using FPGAs and CAD tools for simulation and hardware implementation The more advanced topics and exercises also make this text useful for upper level courses in digital logic programmable logic and embedded systems This new version of the widely used Rapid Prototyping of Digital Systems Second Edition now uses Altera's new Quartus II CAD tool and includes laboratory projects for Altera's UP 2 and the new UP 3 FPGA board Rapid Prototyping of Digital Systems Quartus II Edition includes four tutorials on the Altera Quartus II and NIOS II tool environment an overview of programmable logic and IP cores with several easy to use input and output functions These features were developed to help students get started quickly Early design examples use schematic capture and IP cores developed for the Altera UP FPGA boards VHDL is used for more complex designs after a short introduction to VHDL based synthesis New to this edition is an overview of System on a Programmable Chip SOPC technology and SOPC design examples for the UP3 using Altera's new NIOS II Processor hardware and C software development tools

Tutorial--VLSI Testing & Validation Techniques Hassan K. Reghbati, 1985

Tutorial, VLSI Support Technologies Rex Rice, 1982

Byte, 1986

VLSI Design, 1983

Journal of the Society of Motion Picture and Television Engineers Society of Motion Picture and Television Engineers, 1963

Engineering Application Software, 1986-06-08

Circuit Cellar Ink, 1991

Microprocessor Technical Software, 1984

British universities' guide to graduate study Association of Commonwealth Universities, 1985

The Software Encyclopedia 2001, 2001

Digital Systems Reference Book Brian Holdsworth, Graham R. Martin, 1993 Designed to provide comprehensive coverage of the field of digital systems in a concise but authoritative form For ease of access the book has been divided into five parts fundamentals devices for digital systems system design and techniques system development and applications

Fault-tolerant Computing Dhiraj K. Pradhan, 1986 Fault tolerant computing has evolved into a broad discipline one that encompasses all aspects of reliable computer design Diverse areas of fault tolerant study range from failure mechanisms in integrated circuits to the design of robust software Fault tolerant computing is driven by a number of key factors including ultra high reliability reduced life cycle costs and long

life applications This book is intended to be both introductory and suitable for advanced level graduates Chapters can be selected in various combinations to provide courses with different orientations

Government Reports Announcements,1975 **U.S. Government Research & Development Reports** ,1968 Government Reports Annual Index ,1988

IEEE Transactions on Circuits and Systems ,2006 **BTL Talks and Papers** Bell Telephone Laboratories, inc. Technical Information Libraries,1982 **ESD** ,1988

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