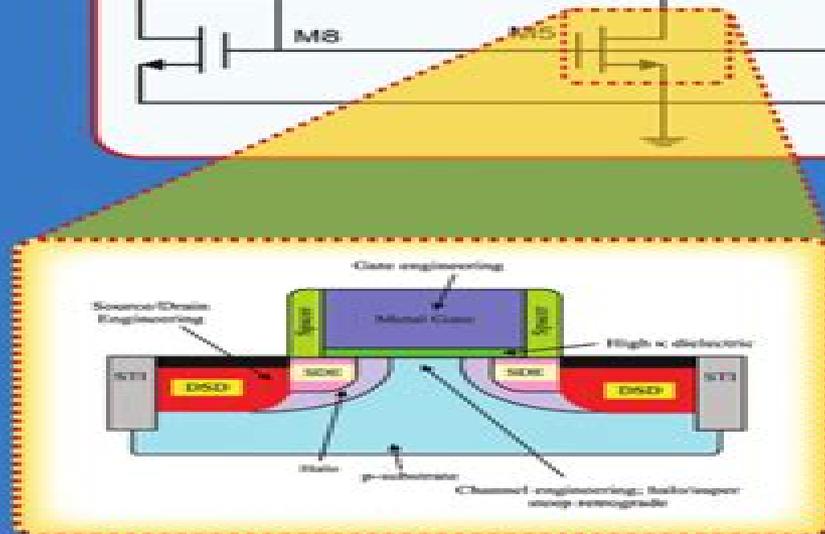
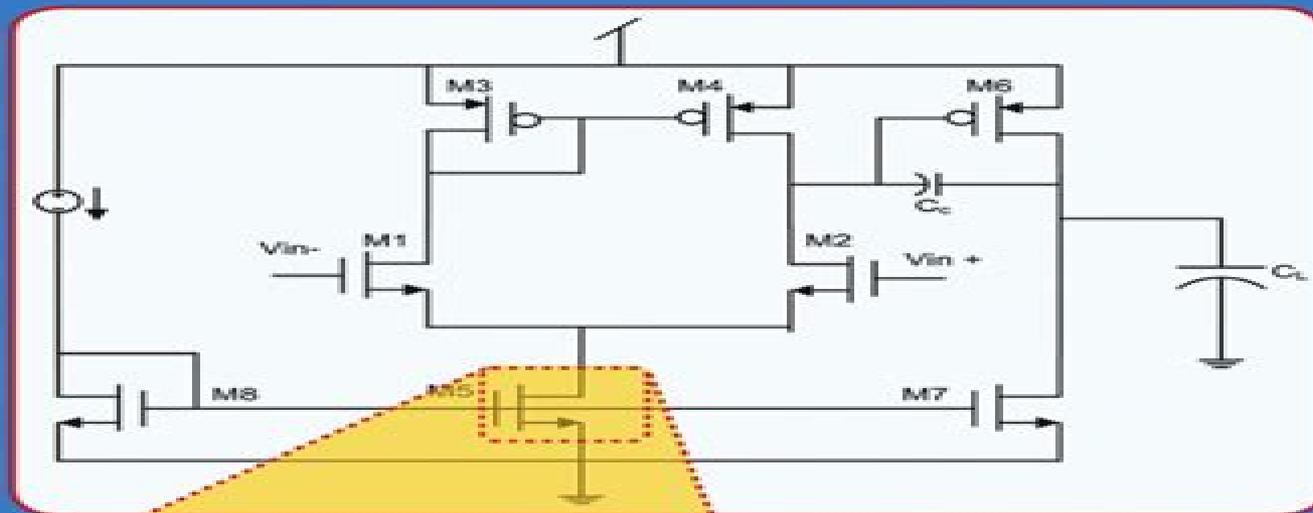


Nano-Scale CMOS Analog Circuits

Models and CAD Techniques for High-Level Design



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Current Mode Analog Integrated Circuits And Linearization Techniques In Cmos Technology

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