Quick Access

1 PlatformIO Debugger
Debug As
Debug Configurations...
Organize Favorites...

Project Explorer

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Debug

- PlatformIO D
  - firmware.e
    - Thread
      - init() at wiring.c:[illegible]
      - main() at main.cpp:35 0x4872
  - platformio -c eclipse debug -d /Volumes/MEDIA/Downloads/eclipse --inte

Expressions | Breakpoints

Expression	Type	Value
i	uint32_t	0
PM	Pm *	0x40000400
CTRL	volatile PM_CTRL_Type	{...}
SLEEP	volatile PM_SLEEP_Type	{...}
Reserved1	const RoRem8 [8]	0x40000402

Name : CTRL
Details:{reg = 0 '\0'}
Default:{...}

main.cpp | main.cpp | wiring.c

```
57   */
58  void init( void )
59  {
60    // Set Systick to 1ms interval, common !
61    if ( SysTick_Config( SystemCoreClock / :
62    {
63      // Capture error
64      while ( 1 ) ;
65    }
66    NVIC_SetPriority (SysTick_IRQn,  (1 << .
67
68    // Clock PORT for Digital I/O
69  //  PM->APBBMASK.reg |= PM_APBBMASK_PORT ;
70  //
71  //  // Clock EIC for I/O interrupts
72  //  PM->APBAMASK.reg |= PM_APBAMASK_EIC ;
73
74    // Clock SERCOM for Serial
75    PM->APBCMASK.reg |= PM_APBCMASK_SERCOM0
76
77    // Clock TC/TCC for Pulse and Analog
78    PM->APBCMASK.reg |= PM_APBCMASK_TCC0 | !
79
80    // Clock ADC/DAC for Analog
81    PM->APBCMASK.reg |= PM_APBCMASK_ADC | PM
82
```

Disassembly | Outline | Variables

Enter location here

```
00004a90:   str     r2, [r3, #32]
 75         PM->APBCMASK.reg |= PM_APBCMASK_S
00004a92:   ldr     r3, [pc, #160]  ; (0x4b34
00004a94:   ldr     r1, [r3, #32]
00004a96:   movs    r2, #252        ; 0xfc
00004a98:   orrs    r2, r1
00004a9a:   str     r2, [r3, #32]
 78         PM->APBCMASK.reg |= PM_APBCMASK_T
00004a9c:   ldr     r1, [r3, #32]
```

Registers

Name	Value
General Registers	
r0	0
r1	0
r2	-2147483648
r3	-536810240
r4	536871200

Name : r0
Hex:0x0
Decimal:0
Octal:0

Writable | Smart Insert | 75 : 25

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