



Republic of the Philippines
 Department of Education
 REGION VI- WESTERN VISAYAS
 CITY SCHOOLS DIVISION OF VICTORIAS
VICTORIAS NATIONAL HIGH SCHOOL

BUSINESS MATH
 1st Quarter
PERFORMANCE TASK 1
(FRACTIONS, DECIMALS, PERCENTAGE)

INSTRUCTION:

Do the following Tasks:

- Task 1 - Find a baker who would be willing to help you. Ask him/her how he/she applies fractions in baking. Illustrate the use of fractions in baking.
- Task 2 - Find a physics teacher who would be willing to help you. Ask him/her how he/she uses decimals in teaching physics to his/her class. Illustrate how the physics uses decimals in his/her lessons to the class.
- Task 3 - Find an Accountant who would be willing to help you. Ask him/her how he/she applies percent in his/her profession. Illustrate how the Accountant uses percent in his/her practice.
- Task 4 - Find a Math teacher who would be willing to help you. Ask him/her how he/she uses the conversion of fractions to decimals and vice versa and the conversion of percent to decimals and vice versa in solving word problems. Illustrate how the Math teacher uses conversion in solving word math problems.

RUBRICS

CRITERIA	LEVELS OF ACHIEVEMENT		
	Sophisticated (3 points each)	Competent (2 points each)	Not Yet Competent (1 point each)
Group Work			
Quality	Information is accurate. Resources are legitimate.	Information is mostly accurate with only a few minor errors. One resource may be questionable.	Information is unbelievable and/or inaccurate. Resources are invalid.
Content of Presentation			
Covers all areas assigned	Areas assigned are covered fully. Able to draw conclusions, make connections, and provide inferences completely.	Majority of area assigned are fully covered. Able to draw conclusions, make connections but inferences are incomplete.	Areas assigned are incompletely covered. Present other information without analysis.
Communication			
Clarity	Presentation is coherent.	Presentation is coherent for the most part, but missing one or two important elements.	Presentation lacks coherence.
Style	Presentation is polished.	Presentation is polished, for the most part, but missing one or two elements.	Presentation is not polished.
Self-Evaluation			
Analysis of group process and individual role within it.	Group members relate well to each other and teamwork is evident.	Group members relate well to each other, but lack teamwork.	Group members do not seem to relate well with one another and lack teamwork.

Chapter 3 Performance Task 1 Geometry

**Jerome S. Gluck, Herbert S.
Levinson, Vergil G. Stover, National
Cooperative Highway Research
Program**

Chapter 3 Performance Task 1 Geometry:

Strategies of Orientation in Environmental Spaces Tobias Meilinger, 2008 **Learning and Instruction** Thomas J. Shuell, Claudia Z. Lee, 1976 Predicting Room of Origin Fire Hazards The Society of Fire Protection Engineers, 2022-08-13

This engineering guide provides a methodology to define and quantify the fire development and ensuing conditions within the room of fire origin from the fire's incipient stage through its full development. The approach presented in this guide was developed using the framework set forth in the SFPE Engineering Guide to Performance Based Fire Protection 2nd ed Quincy Mass National Fire Protection Association 2007. It consists of three distinct parts: 1 Approach selection, 2 Input definition and data collection, 3 Results computation. Specifically, this guide was developed for use as a means to implement the requirements presented in Chapter 10 of the SFPE Engineering Guide to Performance Based Fire Protection. However, material within this guide has broader applicability and is therefore not limited to performance based design applications. *Publication - National Academy of Sciences-National Research Council*, 1961 Analog Layout Generation for Performance and Manufacturability Koen Lampaert, Georges Gielen, Willy Sansen, 1999-04-30

Analog integrated circuits are very important as interfaces between the digital parts of integrated electronic systems and the outside world. A large portion of the effort involved in designing these circuits is spent in the layout phase. Whereas the physical design of digital circuits is automated to a large extent, the layout of analog circuits is still a manual, time-consuming and error-prone task. This is mainly due to the continuous nature of analog signals which causes analog circuit performance to be very sensitive to layout parasitics. The parasitic elements associated with interconnect wires cause loading and coupling effects that degrade the frequency behaviour and the noise performance of analog circuits. Device mismatch and thermal effects put a fundamental limit on the achievable accuracy of circuits. For successful automation of analog layout, advanced place and route tools that can handle these critical parasitics are required. In the past, automatic analog layout tools tried to optimize the layout without quantifying the performance degradation introduced by layout parasitics. Therefore, it was not guaranteed that the resulting layout met the specifications and one or more layout iterations could be needed. In Analog Layout Generation for Performance and Manufacturability, the authors propose a performance-driven layout strategy to overcome this problem. In this methodology, the layout tools are driven by performance constraints such that the final layout with parasitic effects still satisfies the specifications of the circuit. The performance degradation associated with an intermediate layout solution is evaluated at runtime using predetermined sensitivities. In contrast with other performance-driven layout methodologies, the tools proposed in this book operate directly on the performance constraints without an intermediate parasitic constraint generation step. This approach makes a complete and sensible trade-off between the different layout alternatives possible at runtime and therefore eliminates the possible feedback route between constraint derivation, placement and layout extraction. Besides its influence on the performance layout, it also has a profound impact on the yield and testability of an analog circuit. In Analog

Layout Generation for Performance and Manufacturability the authors outline a new criterion to quantify the detectability of a fault and combine this with a yield model to evaluate the testability of an integrated circuit layout They then integrate this technique with their performance driven routing algorithm to produce layouts that have optimal manufacturability while still meeting their performance specifications Analog Layout Generation for Performance and Manufacturability will be of interest to analog engineers researchers and students **Scott Foresman-Addison Wesley Middle School Math**, 2002 **Evaluating Pavement Strategies and Barriers for Noise Mitigation** Paul R. Donovan, 2013 TRB's National Cooperative Highway Research Program NCHRP Report 738 Evaluating Pavement Strategies and Barriers for Noise Mitigation presents a methodology for evaluating feasibility reasonableness effectiveness acoustic longevity and economic features of pavement strategies and barriers for noise mitigation The methodology uses a life cycle cost analysis to examine the economic features of mitigation alternatives the FHWA Traffic Noise Model to integrate the noise reduction performance of pavements and barriers and on board sound intensity measurements as an input to the prediction model The appendixes contained in the research agency's final report provide elaborations and detail on several aspects of the research The appendixes are not included with the print version of the report but are available online Publisher description **The Interaction Between Geometry and Performance of a Centrifugal Pump** B. Neumann, 1991 The design of hydraulic machinery in general and of centrifugal pumps in particular has been and still is essentially empirical One reason for this is the great variety of types sizes and uses of centrifugal pumps resulting in a great multiplicity of design considerations **Elementary and Intermediate Algebra** Stefan Baratto, Barry Bergman, Donald Hutchison, 2013-05-17 Elementary and Intermediate Algebra 5th edition by Baratto Bergman and Hutchison is part of the latest offerings in the successful Hutchison Series in Mathematics The book is designed for a two semester course sequence in beginning algebra and intermediate algebra is appropriate for lecture learning center laboratory and self paced settings The fifth edition continues the series hallmark approach of encouraging mastery of mathematics through careful practice The text provides detailed straightforward explanations and accessible pedagogy to help students grow their math skills from the ground up The authors use a three pronged approach of communication pattern recognition and problem solving to present concepts understandably stimulate critical thinking skills and stress reading and communication skills in order to help students become effective problem solvers Features such as Tips for Student Success Check Yourself exercises and Activities underscore this approach and the underlying philosophy of mastering math through practice Exercise sets have been significantly expanded and are now better organized and applications are now more thoroughly integrated throughout the text The text is fully integrated with McGraw Hill's new online learning system Connect Math Hosted by ALEKS Corp and is available with ALEKS 360 **Student Response in Programmed Instruction** Arthur A. Lumsdaine, 1961 **Instructor's Manual for Elementary and Middle School Mathematics** John Van de Walle, 2004 **Summary of Progress - National Cooperative Highway Research Program**

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needed by engineers working with electrical systems **Math Advantage** Grace M. Burton,1999 *A Parent's Guide to
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