

operating bias region for each condition?



Figure P3.27

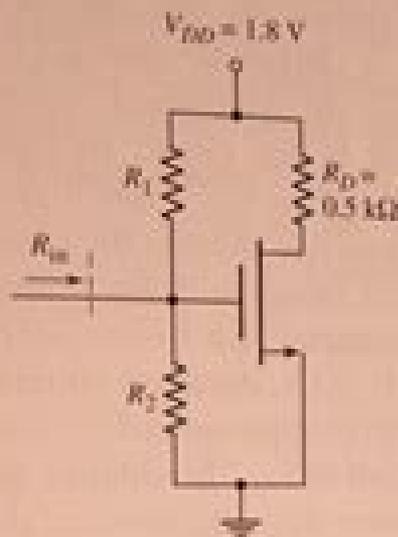


Figure P3.28



Figure P3.29

Figure P3.26

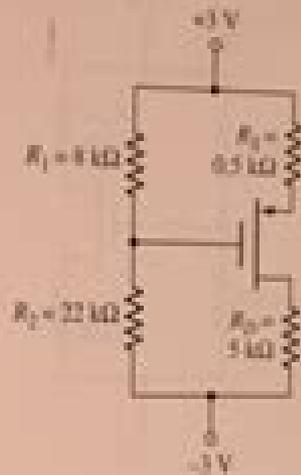


Figure P3.30

13.28 The transistor in Figure P3.28 has parameters $V_{TN} = 0.4 \text{ V}$, $k'_n = 120 \mu\text{A/V}^2$, and $W/L = 80$. Design the circuit such that $I_Q = 0.8 \text{ mA}$ and $R_s = 200 \text{ k}\Omega$.

13.29 The transistor in the circuit in Figure P3.29 has parameters $V_{TF} = -0.8 \text{ V}$ and $K_p = 0.20 \text{ mA/V}^2$. Sketch the load line and plot the Q -point for (a) $V_{DD} = 3.5 \text{ V}$, $R_D = 1.2 \text{ k}\Omega$ and (b) $V_{DD} = 5 \text{ V}$, $R_D = 4 \text{ k}\Omega$. What is the operating bias region for each condition?

13.30 Consider the circuit in Figure P3.30. The transistor parameters are $V_{TF} = -0.8 \text{ V}$ and $K_p = 0.5 \text{ mA/V}^2$. Determine I_D , V_{SG} , and V_{SD} .

13.31 For the circuit in Figure P3.31, the transistor parameters are $V_{TF} = -0.8 \text{ V}$ and $K_p = 200 \mu\text{A/V}^2$. Determine V_S and V_{SD} .

13.32 Design a MOSFET circuit in the configuration shown in Figure P3.26. The transistor parameters are $V_{TN} = 0.4 \text{ V}$ and $k'_n = 120 \mu\text{A/V}^2$, and $\lambda = 0$. Design the circuit such that $I_Q = 0.8 \text{ mA}$ and $R_s = 5 \text{ k}\Omega$. Design the circuit such that $V_{GS} = 0.8 \text{ V}$. Set

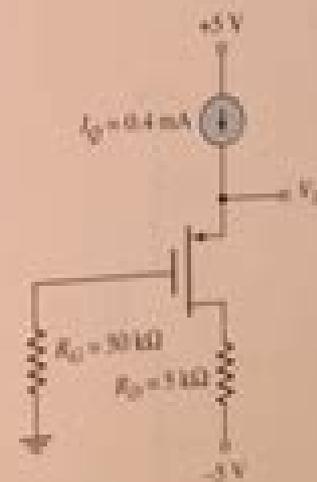


Figure P3.31

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Peipei Pang



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 - The Rise of Digital Reading Microelectronics Circuit Analysis And Design 4th Edition Solutions Chapter 3
 - Advantages of eBooks Over Traditional Books
2. Identifying Microelectronics Circuit Analysis And Design 4th Edition Solutions Chapter 3
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
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 - Popular eBook Platforms
 - Features to Look for in an Microelectronics Circuit Analysis And Design 4th Edition Solutions Chapter 3
 - User-Friendly Interface
4. Exploring eBook Recommendations from Microelectronics Circuit Analysis And Design 4th Edition Solutions Chapter 3
 - Personalized Recommendations
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 - Microelectronics Circuit Analysis And Design 4th Edition Solutions Chapter 3 and Bestseller Lists
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 - Highlighting and Note-Taking Microelectronics Circuit Analysis And Design 4th Edition Solutions Chapter 3
 - Interactive Elements Microelectronics Circuit Analysis And Design 4th Edition Solutions Chapter 3
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 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Microelectronics Circuit Analysis And Design 4th Edition Solutions Chapter 3
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 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Microelectronics Circuit Analysis And Design 4th Edition Solutions Chapter 3
10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
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 - Setting Reading Goals Microelectronics Circuit Analysis And Design 4th Edition Solutions Chapter 3
 - Carving Out Dedicated Reading Time
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 - Fact-Checking eBook Content of Microelectronics Circuit Analysis And Design 4th Edition Solutions Chapter 3
 - Distinguishing Credible Sources
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 - Utilizing eBooks for Skill Development
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