

SYSTEM-LEVEL TEST TECHNIQUES

INTRODUCTION

- In the 1970s, the in-circuit testing (ICT) method appeared.
- Mechanical testing becomes difficult with board trace widths and separations
 - below 0.1 mm or 100 μm .
- In 1985 a group of European manufacturer formed the Joint European Test Action Group (JETAG) to study board testing.
- In 1986 JETAG becomes Joint Test Action Group (JTAG) with the addition of North American Companies.
- The main virtue of the 1149.1 standard can be used by board designers, IC designers, and systems designers.
- without the need for members of each design community to fully understand the testing problems of the other communities.

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Selma Kurra



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