

Transient Response of RC and RL Circuits

Objective

- Observe, and compare with theory, the transient response of RL, RC and RLC circuits under various damping conditions.
- To develop skills in circuit analysis and data collection
- To improve report writing

Theory:

1) Transient behavior of RC circuits

For the RC circuit in Figure 9.1 assume that the power supply is $V_i(t) = V_s \cdot u(t)$ volts, where $u(t)$ is the unit step function, i.e., $u(t) = 0, t < 0$ and $u(t) = 1, t \geq 0$.

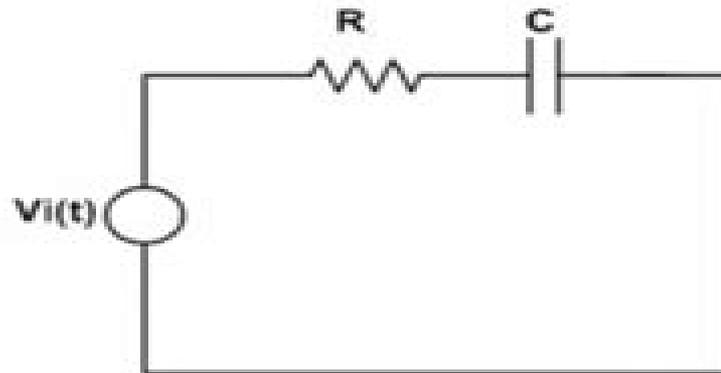


Figure 9.1 - Series RC circuit.

Then, the capacitor voltage transient equation can be written as

$$V_C(t) = V_C(\infty) + \left[V_C(0) - V_C(\infty) \cdot e^{\left(\frac{-t}{T_c}\right)} \right] \quad (9.1)$$

Where $T_c = RC$ is the time constant and $V_C(0)$ and $V_C(\infty)$ are the initial and final voltages across the capacitor at time $t = 0$ and $t = \infty$, respectively.

2) Transient behavior of RL circuits

For the RL circuit in Figure 9.2, assume that the power supply is $V_i(t) = V_s \cdot u(t)$ volts.

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D. Roy Choudhury, Shail Jain



Circuit Analysis Objective Questions Transient Response:

Network Theory: Analysis and Synthesis : For the University of Mumbai Ravish R. Singh, This book is core to the understanding of engineering of Electronics and Telecommunications and hence it becomes an important subject for students of Electronics Telecommunication Engineering and Electronics Engineering in their Third Semester A strong conceptual understanding of the subject is what the textbook lends to its reader and an apart from an emphasis on problem solving approach and discussion on both analysis and synthesis of networks It offers ample coverage of DC circuits network theorems transient analysis two port networks and network synthesis among other major topics

Circuit and Network Theory GATE, PSUS AND ES Examination Satish K Karna, Test Prep for Circuit and Network Theory GATE PSUS AND ES Examination

NETWORK ANALYSIS AND SYNTHESIS KUMAR, A. ANAND, 2019-01-01 This comprehensive test on Network Analysis and Synthesis is designed for undergraduate students of Electronics and Communication Engineering Electrical and Electronics Engineering Electronics and Instrumentation Engineering Electronics and Computer Engineering and Biomedical Engineering The book will also be useful to AMIE and IETE students Written with student centered pedagogically driven approach the text provides a self centered introduction to the theory of network analysis and synthesis Striking a balance between theory and practice it covers topics ranging from circuit elements and Kirchhoff's laws network theorems loop and node analysis of dc and ac circuits resonance transients coupled circuits three phase circuits graph theory Fourier and Laplace analysis Filters attenuators and equalizers to network synthesis All the solved and unsolved problems in this book are designed to illustrate the topics in a clear way

KEY FEATURES Numerous worked out examples in each chapter Short questions with answers help students to prepare for examinations Objective type questions Fill in the blanks Review questions and Unsolved problems at the end of each chapter to test the level of understanding of the subject Additional examples are available at www.phindia.com/anand_kumar_network_analysis

Network Analysis and Synthesis Mohammed Arshad, 2006-06

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A First Circuits Course for Engineering Technology Charles Belove, 1982

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